

**BEST AVAILABLE COPY****IN THE ABSTRACT**

Please replace the Abstract with the following paragraph:

A digital signal processing apparatus includes multiple functional units each adapted to execute operations, and control circuitry for controlling the functional units. The control circuit includes multiple control units, at least one control unit being operatively associated with a respective functional unit for controlling its function, and each functional unit is adapted to execute operations in an autonomous manner under control of the control unit associated therewith. FIFO (first-in/fist-out) register circuitry may be provided for supporting data-flow communication among said functional units.

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